TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOSIII)

# **TPC6104**

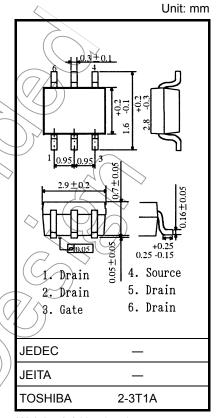
### Notebook PC Applications

Portable Equipment Applications

- Low drain-source ON resistance:  $R_{DS (ON)} = 33 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance:  $|Y_{fS}| = 12 \text{ S (typ.)}$
- Low leakage current:  $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -20 \text{ V)}$
- Enhancement mode:  $V_{th} = -0.5$  to -1.2 V  $(V_{DS} = -10$  V,  $I_D = -200$   $\mu$ A)

### **Absolute Maximum Ratings (Ta = 25°C)**

Characteristics			Symbol	Rating	Unit
Drain-source voltage			V <sub>DSS</sub>	-20	A
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )			V <sub>DGR</sub>	-20	y
Gate-source voltage			V <sub>GSS</sub>	±8(	> v
Drain current	DC (No	te 1)	ID	-5.5	А
Dialii Cuitent	Pulse (No	te 1)	I <sub>DP</sub>	-22	A
Drain power dissipation (t = 5 s) (Note 2a)			P <sub>D</sub>	2.2	/W
Drain power dissipation $(t = 5 s)$ (Note 2b)			PD	0.7	W
Single pulse avalanche energy (Note 3)			EAS	4.9	√ mJ
Avalanche current			(lar))	-2.75	A
Repetitive avalanche energy (Note 4)			EAR	0.22	mJ
Channel temperature			Tch	150	<b>∵</b> °C
Storage temperature range			T <sub>stg</sub>	-55~150	°C



Weight: 0.011 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data/(i.e. reliability test report and estimated failure rate, etc).

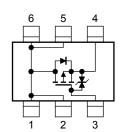
#### Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient $(t > 5 s)$ (Note 2a)	R <sub>th (ch-a)</sub>	56.8	°C/W
Thermal resistance, channel to ambient (t = 5 s) (Note 2b)	R <sub>th (ch-a)</sub>	178.5	°C/W

Note: (Note 1), (Note 2), (Note 3) and (Note 4): See the third page.

This transistor is an electrostatic-sensitive device. Please handle with caution.

#### **Circuit Configuration**



## **Electrical Characteristics (Ta = 25°C)**

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 8 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μА
Drain cut-off curr	ent	I <sub>DSS</sub>	$V_{DS} = -20 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА
Drain-source breakdown voltage		V <sub>(BR)DSS</sub>	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-20	_		>
		V <sub>(BR)DSX</sub>	$I_D = -10 \text{ mA}, V_{GS} = 8 \text{ V}$	12	_	_	v
Gate threshold ve	oltage	V <sub>th</sub>	$V_{DS} = -10 \text{ V}, I_D = -200 \mu\text{A}$	-0.5		-1.2	>
Drain-source ON resistance		R <sub>DS (ON)</sub>	$V_{GS} = -1.8 \text{ V}, I_D = -1.4 \text{ A}$	) /\	78	120	
		R <sub>DS</sub> (ON)	$V_{GS} = -2.5 \text{ V}, I_D = -2.8 \text{ A}$	$\rightarrow$	49	60	mΩ
		R <sub>DS</sub> (ON)	$V_{GS} = -4.5 \text{ V}, I_D = -2.8 \text{ A}$	\	33	40	
Forward transfer admittance		Y <sub>fs</sub>	$V_{DS} = -10 \text{ V}, I_D = -2.8 \text{ A}$	6	12		S
Input capacitance		C <sub>iss</sub>			1430	1	
Reverse transfer capacitance		C <sub>rss</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	200	<i>/</i>	pF
Output capacitance		Coss		-	240	> —	
Switching time	Rise time	t <sub>r</sub>			8.5	) —	
	Turn-on time	t <sub>on</sub>	VGS 5 VOUT	) / (	> 15		
	Fall time	t <sub>f</sub>	4.7.00 W		20		ns
	Turn-off time	t <sub>off</sub>	Duty ≤ 1%, t <sub>w</sub> ≠ 10 μs	_	66	_	
Total gate charge (gate-source plus gate-drain)		(Q <sub>g</sub>	$V_{DD} \simeq -16 \text{ V}, V_{GS} = -5 \text{ V},$		19		
Gate-source charge		Qgs	$I_D = -5.5 \text{ A}$	_	14	_	nC
Gate-drain ("miller") charge		$Q_{gq}$		_	5	_	

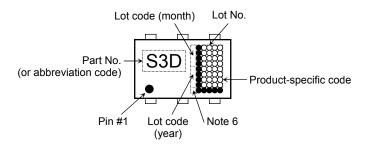
# Source-Drain Ratings and Characteristics (Ta = 25°C)

Charact	teristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	\ IDRP \	<del>-</del>	_	_	-22	Α
Forward voltage4	(diode)	V <sub>DSF</sub>	$I_{DR} = -5.5 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	1.2	V

25.4 × 25.4 × 0.8

Unit: (mm)

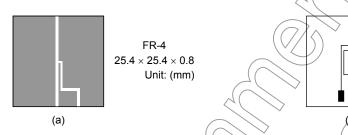
#### Marking (Note 5)



Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: (a) Device mounted on a glass-epoxy board (a) (t = 5 s)

(b) Device mounted on a glass-epoxy board (b) (t = 5 s)



Note 3: VDD = 16 V,  $Tch = 25^{\circ}\text{C}$  (initial), L = 0.5 mH,  $RG = 25 \Omega$ , IAR = 2.75 A

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

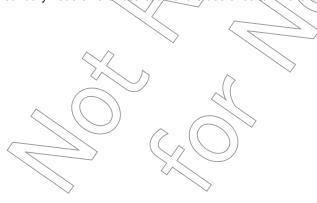
Note 6: A dot marking for identifying the indication of product Labels.

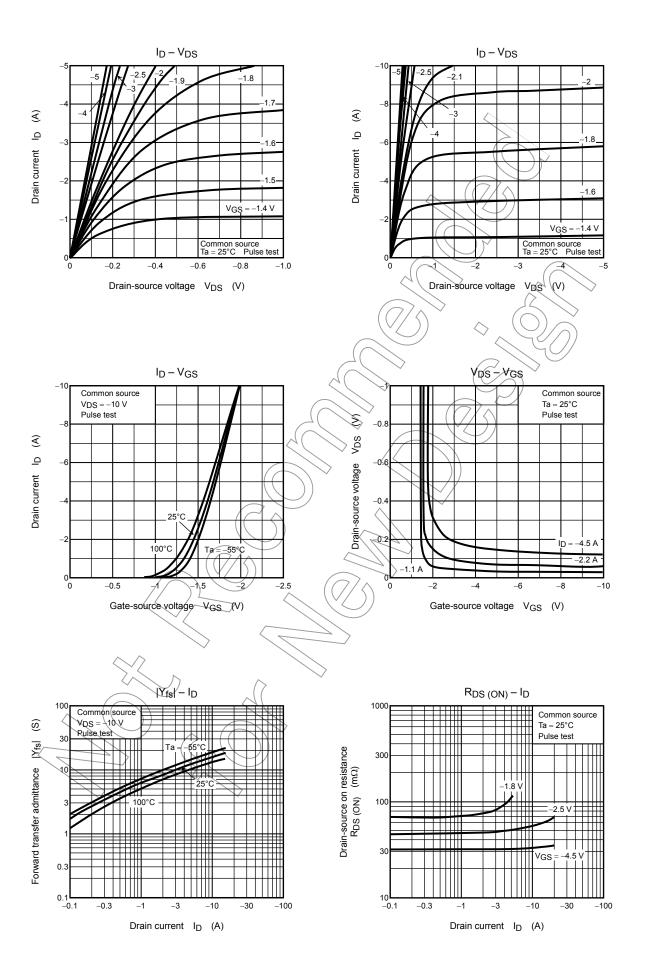
Without a dot: [[Pb]]/INCLUDES > MCV

With a dot: [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

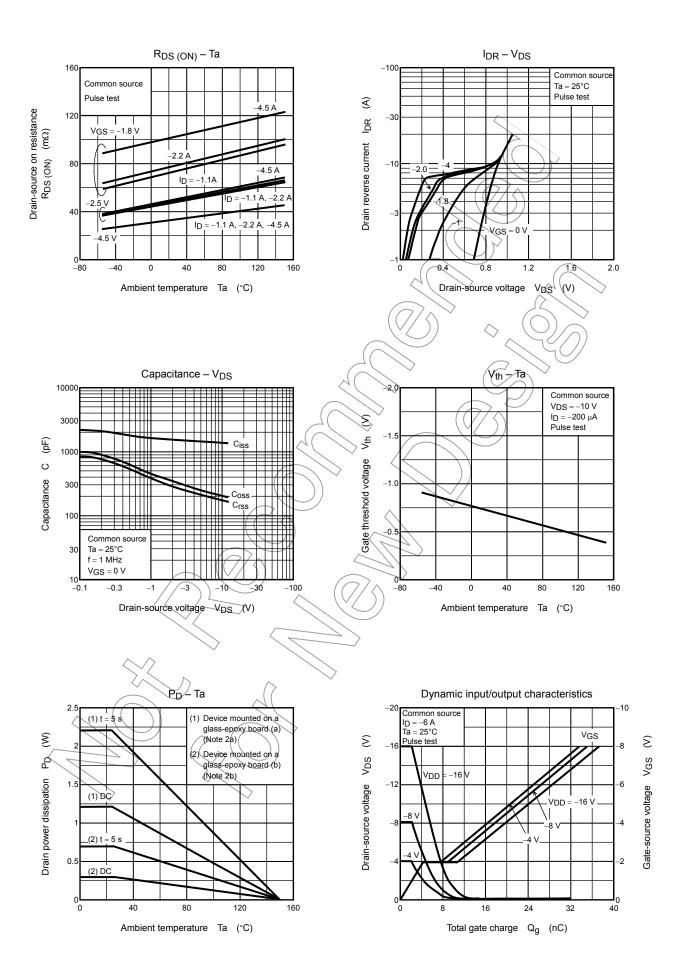
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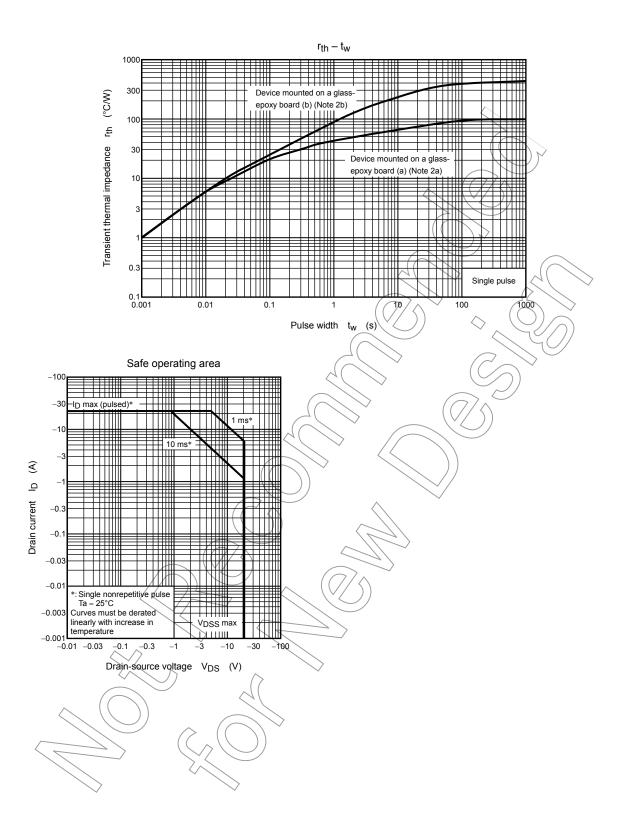
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